

## **2012 ISSCC Distinguished Technical Paper**

*"A Blocker-Tolerant Wideband Noise-Cancelling Receiver with a 2dB Noise Figure"*

David Murphy<sup>1,2</sup>, Amr Hafez<sup>1,2</sup>, Ahmad Mirzaei<sup>2</sup>, Mohyee Mikhemar<sup>2</sup>, Hooman Darabi<sup>2</sup>, Mau-Chung Frank Chang<sup>1</sup>, Asad Abidi<sup>1</sup>

<sup>1</sup>University of California, Los Angeles, Los Angeles, CA

<sup>2</sup>Broadcom, Irvine, CA

## **2012 ISSCC Award for Distinguished Technical Paper**

*"A 280mV-to-1.1V 256b Reconfigurable SIMD Vector Permutation Engine with 2-Dimensional Shuffle in 22nm CMOS"*

Steven Hsu, Amit Agarwal, Mark Anders, Sanu Mathew, Himanshu Kaul, Farhana Sheikh, Ram Krishnamurthy

Intel, Hillsboro, OR

## **2011 Lewis Winner Award for Outstanding Paper**

*"An Angle-Sensitive CMOS Imager for Single-Sensor 3D Photography"*

Albert Wang, Patrick R. Gill, Alyosha Molnar

Cornell University, Ithaca, NY

## **2011 Jan Van Vessel Award for Outstanding European Paper**

*"A 4GHz CT DS ADC with 70dB DR and -74dBFS THD in 125MHz BW"*

Muhammed Bolatkale<sup>1</sup>, Lucien J. Breems<sup>1</sup>, Robert Rutten<sup>1</sup>, Kofi A.A. Makinwa<sup>2</sup>

<sup>1</sup>NXP Semiconductors, Eindhoven, The Netherlands

<sup>2</sup>Delft University of Technology, Delft, The Netherlands

## **2011 ISSCC Award for Outstanding Forum Presenter**

*"Current-Steering DACs for Direct RF Transmission"*

Klaas Bult

Broadcom, Bunnik, The Netherlands

## **2011 Evening Session Award**

*“Future System and Memory Architectures: Transformations by Technology and Applications”*

Co-OrganizerChair:

Nicky Lu, Etron Technology, Hsinchu, Taiwan

Co-Organizers:

Leland Chang, IBM, Yorktown Heights, NY

Daisaburo Takashima, Toshiba, Yokohama, Japan

Speakers:

Jim Kahle, Austin, TX

Stephen Pawlowski, Intel, Hillsboro, Japan

Tomofumi Shimada, Toshiba, Tokyo, Japan

Raj Talluri, Qualcomm, San Diego, CA

## **2012 ISSCC Silkroad Award**

*“A 15mW 3.6GS/s CT-DS ADC with 36MHz Bandwidth and 83dB DR in 90nm CMOS”*

Pradeep Shettigar

IIT Madras, Chennai, India

*“An 800MHz 320mW 16-Core Processor with Message-Passing and Shared-Memory Inter Core Communication Mechanisms”*

Ruijin Xiao

Fudan University, China